TRIMONDTM MOTHERBOARDS

Trimond IN440 µATX

QuickStart Guide

CPU

The Trimond IN440 Micro ATX motherboard uses Intel's 440ZX chipset, which is optimised for Pentium[®]II / Pentium[®]III **100MHz Front Side Bus** processors, but which also support 66MHz Pentium[®]II / Pentium[®]III CPUs. Celeron support is achieved by implementing an alternative microcode from the BIOS update disk. Please contact Mitsubishi if you require assistance with this. Processor core multipliers can be set using jumper block **J1**. Core voltage is set by the processor. Location of J1 shown below.



CPU speed jumper block J1 settings

Ratio	Α	В	С	D	66 / 100 MHz frequency
					local bus
2/7	on	Off	off	on	233 / 350
1 / 4	on	On	on	off	266 / 400
2/9	on	Off	on	off	300 / 450
1/5	on	On	off	off	333 / 500
2/11	on	Off	off	off	366 / 550

RAM

Two DIMM sockets accept 64-bit wide unbuffered **PC100 SDRAM** modules (or standard 66MHz SDRAM for 66MHz FSB Pentium[®]II CPUs) with **SPD** (serial presence detect). Please note that EDO memory is <u>NOT</u> supported.

Populate DIMM socket **MM2** first, then MM1.

Supported sizes: 16MB, 32MB, 64MB, 128MB, 256MB.

Approved vendors: Please see the current list at http://www.trimond.com/shared/reference.asp.

KEYBOARD & MOUSE

Orientation as shown.





PSU

Trimond motherboards are designed to be connected to a **soft-switch PSU**, with 5V standby. If the 5V standby current is insufficient, the PSU may deregulate and possibly damage the motherboard. Ensure that at least **30mA** is available to the 5V standby output.

If you wish to use a **hard-switch PSU**, it is necessary to change a flag in the manufacturer settings of the BIOS and provide current to the motherboard 5V standby connector. Please contact the Motherboard Division for full instructions.

Connect the power on/off lead to "PWR ON" on PL9.-

COOLING

Two fan connectors are available: **PL2** & **PL3**. PL3 (Fan 2) provides **full-speed** fan operation. PL3 is not included on Rev B evaluation boards. PL2 (CPU Fan) is regulated by the power management software (Windows 98/ACPI). On "basic" configuration boards, PL2 operates at full speed.





BIOS updates are available from our web site. Please see *Documentation & Drivers* below for further details.

The port 80 codes for Phoenix BIOS 4.0 Rel 6.1 can be obtained from our web site:

http://www.trimond.com/shared/reference.asp

CMOS may be cleared using PL6 (Battery Mem Clear). Remove AC mains. Move jumper from right (1-2) to left (2-3) pins for 3 seconds. Move jumper to original position.

PL6 ----



DOCUMENTATION & DRIVERS

Supporting documentation, drivers and BIOS updates are available from our web site as follows:

http://www.trimond.com

- Click on the "Support" left menu item.
- Click on "Private Pages".
- Login using the following user-id:

Name: **evaluation** (not case-sensitive) Password: **motherboard** (not case-sensitive)

An online problem report form is available from the evaluation support page.

