PT 726

SECTION 3

LAYOUT DIAGRAM

J118
Keyboard Connector
Power Connector
SIMM3
SIMM2
SIMM1
SIMM0

PCI3 PCI2 PCI1
J2
J114
J112
J111
J110
J115
J113 JK3
S82378
S82434
S82433
S82433
J47
J46
J70 Y1
J72/J71
PENTIUM
60/66.66MHZ
KLOCK
RESET
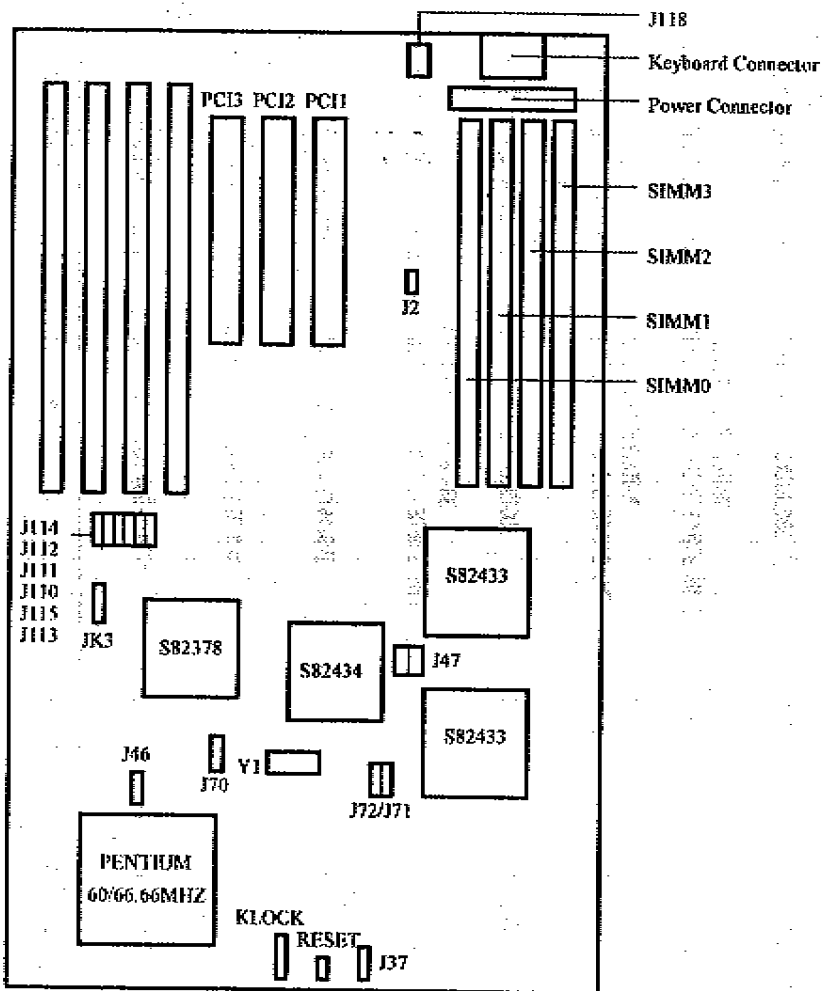J37

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SECTION 4

BIOS Setup

4.1 About CMOS Setup

Once the mainboard has been integrated into a system, you must run the mainboard's Setup program to record and/or change configuration information, such as the current date and time or your hard disk drive type. The Setup program is stored in read-only memory (ROM), and can only be accessed when you turn on or reset the system.

The settings you specify with the Setup program are recorded in a special area of memory called CMOS RAM. This memory is backed up by a component called *Real Time Clock Plus Ram* so that it will not be erased when you turn off or reset the system. Whenever you turn on the power, the system reads the settings stored in CMOS RAM and compares them to the equipment check conducted during the POST. If an error occurs, an error message will be displayed on screen and you will be prompted to run the Setup program.

The BIOS Setup program is accessed through a menu which allows you to easily configure your system. Standard CMOS Setup allows you to record basic system information regarding date and time, video type and installed drives. Advanced CMOS Setup gives you access to the advanced features supported by the BIOS and your mainboard's Mercury Chipset.

4.2 About WinBIOS

American Megatrends new WinBIOS includes all features from pervious versions of the AMIBIOS. It has a new system configuration utility with an easy-to-use graphical user interface, WinBIOS Setup.

The configuration options in WinBIOS Setup are all icon-based. All settings for each option are displayed for easy access. All WinBIOS Setup functions can be accessed by mouse and keyboard.

The system BIOS (Basic Input Output System) is the interface between the hardware and the operating system software used in all IBM compatible computers. The primary function of the system Bios is to provide a series of software interrupts, functions, and subfunctions that control operations on certain devices, such as the hard disk drive, floppy drive, and video subsystem.

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SECTION 2

SPECIFICATIONS

2.2 Jumpers and Connectors

J2 :   Clear CMOS Data Jumper

This Jumper is used to clear the system configuration data currently stored in the CMOS RAM and reload the default system configuration setting.

Jumper	Description	Remark
Open *	DS1287/DS1287A	Default
Close	DS1287 Cannot Work DS1287A Clear the CMOS Data	

Remark : If the real time clock module is DS1287A, the Cmos jumper must be shorted while the computer is powered OFF. Shorting the jumper while the machine is powered on may result in damage the DS1287A. Other compatible modules may be used.

J46 : Operation mode in Internal Cache

Jumper	Description
Open *	Internal Cache in write back
Short	Internal Cache in write through

J47 : Parity Support

Jumper	Description
1-2 & 3-4 *	Support Parity
1-3 & 2-4	Disable Support Parity

J70 :   SRAM Type

Jumper	Description	Remark
1-2 *	Standard SRAM	Default
2-3	Burst SRAM	

J71 / J72 : External Cache Setting

Cache Size	J72	J71
No Cache	1-2	1-2
Reserved	1-2	2-3
256K *	2-3	1-2

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SPECIFICATIONS

2.2 Jumpers and Connectors

J110-J115 : Interrupt Select

Jumper	Description
J110	IRQ10
J111	IRQ11
J112 *	IRQ14
J113	IRQ3
J114	IRQ15
J115	IRQ5

J118 : Powergood Select

Jumper	Description	Remark
1-2	On board Powergood	
2-3 *	Power Supply Powergood	Default

Remark : If the Power Supply is unsteady, user can select the on-board powergood signal.

JK3 : EPROM Select

Jumper	Description	Remark
1-2	27C010 / 27C020 EPROM	
2-3 *	27C512 / 27C010 EPROM	Default

* is the default setting.

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SPECIFICATIONS

2.13 Configuration of System IRQs

There are three PCI Slots on the PT-726 and the slots are defined as follows,

PCI Slot	PCI Interrupt	Description
1	INTA	Master or Slave
2	INTA	Master or Slave
3	INTA	Master or Slave

For INTA Signal, only one IRQ3, IRQ5, IRQ10, IRQ11, IRQ14, IRQ15 should be connected.

If you use a PCI IDE Card on a PCI Slot, the INTA of the PCI Slot must be connected to IRQ14. If you want to use ISA IDE Card, IRQ14 must be kept open.

You can also insert other PCI peripheral cards, e.g. PCI VGA on other PCI Slot provided that this PCI card do not need an Interrupt Request.

2.2 Jumpers and Connectors

Jumpers / Connectors	Description
PWR	Power Connector
KEYCON	Keyboard Connector
RESET	Reset
KLOCK	Power LED and Keylock
J37	Speaker
J2	Clear CMOS Data Jumper
J46	Operation mode in Internal Cache
J47	Parity Support
J70	SRAM Type
J71 & J72	External Cache Setting
J110-J115	Interrupt Select
J118	Powergood Select
JK3	EPROM Select

SECTION 2

SPECIFICATIONS

2.2 Jumpers and Connectors

PWR :   Power Connector

Pin	Description
1	Power Good
2	+5V
3	+12V
4	-12V
5	Gnd
6	Gnd
7	Gnd
8	Gnd
9	-5V
10	+5V
11	+5V
12	+5V

RESET : Hardware Reset

Pin	Description
1	Reset
2	Gnd

KLOCK : Power LED and Keylock

Pin	Description
1	LED Power
2	N.C.
3	Gnd
4	Keyboard Inhibit
5	Gnd

J37 : Speaker

Pin	Description
1	Signal
2	N.C.
3	Gnd
4	+5V