

Jumper Settings for Cyrix Processors

SW1: Cyrix Processors - External Bus Clock - Frequency Ratio	
6x86L PR150+ - 60MHz - 2x 	6x86MX-PR233 - 75MHz - 2.5x
6x86L PR166+ - 66MHz - 2x 	6x86MX-PR266 - 83MHz - 2.5x
6x86MX-PR166 - 60MHz - 2.5x 	M II-300 - 75MHz - 3x
6x86MX-PR200 - 66MHz - 2.5x 	M II-300 - 66MHz - 3.5x
6x86MX-PR200 - 75MHz - 2x 	

Jumper Settings for IDT Processors

SW1: IDT Processors - External Bus Clock - Frequency Ratio		
C6-180 - 60MHz - 3x 	C6-200 - 66MHz - 3x 	C6-225 - 75MHz - 3x

Jumper Settings for AMD Processors

SW1: AMD Processors - External Bus Clock - Frequency Ratio		
K5 PR90 - 60MHz - 1.5x 	K5 PR166 - 66MHz - 1.75x 	K6-266 - 66MHz - 4x
K5 PR100 - 66MHz - 1.5x 	K6-166 - 66MHz - 2.5x 	K6-2/266 - 66MHz - 4x
K5 PR120 - 60MHz - 1.5x 	K6-200 - 66MHz - 3x 	K6-300 - 66MHz - 4.5x
K5 PR133 - 66MHz - 1.5x 	K6-233 - 66MHz - 3.5x 	

Jumper Settings for Core Voltage

SW2: Voltage	
2.2V 	3.2V
2.8V* 	3.3V
2.9V 	3.5V

* Default

Note : 1. IDT processors are single voltage processors and must be set at 3.5V.

2. The voltage of AMD K6-266/300 must be set at 2.2V.