

VESA Bus Connector

The cache system board provides two high-performance VESA bus connectors, SL15 and SL16, for use with VESA peripherals. These connectors can be utilized for one Local Bus Master and one Local Bus Slave, either (SL15) or (SL16).

The following tables give the pin assignments for SL15 and SL16. Side A of the connector are pin outs on the board's component side while Side B are pin outs on the board's solder side. Jumpers JV2 and JV3 give more information on settings on the mainboard and the VL-bus controller.

JUMPER	PIN DEFINITION
JV2	CPU Speed Select 1-2 Greater than 33 MHz 2-3 Less than or equal to 33 MHz
JV3	High Speed Write 1-2 One wait write 2-3 Zero wait write (default)