

CONNECTOR	PIN OUTS	SIGNAL NAME
J13 Keylock and Power LED Connector	1	Power signal
	2	Spare
	3, 5	Ground
	4	Keylock

Table 2-5. Connector Pin Definitions

VESA Bus Connector

The cache system board provides two high-performance VESA bus connectors, SL14 and SL15, for use with VESA peripherals. These connectors can be utilized for Local bus (SL14) and (SL15).

→ **NOTE :** The two VESA Local Bus slot can accommodate either one VESA Master with one VESA Slave or two VESA Slaves.

The following tables give the pin assignments for SL14 and SL15. Side A of the connector are pin outs on the board's component side while Side B are pin outs on the board's solder side. Jumpers JV1 and JV2 give more information on settings on the mainboard and the VL-bus controller.

JUMPER	PIN DEFINITION
JV1	CPU Speed Select
	1-2 Greater than 33 MHz
	2-3 Less than or equal to 33 MHz
JV2	High Speed Write
	1-2 One wait write
	2-3 Zero wait write (default)