

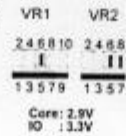
VT-501 Addendum

Cyrix and IBM's 6x86/MX series (page 27, 29)

Model Name	CPU Speed	External (CPUCLK)	CLK1	CLK2	CLK3	CPU Clock Rate		
						Internal	FREQ1	FREQ2
6x86MX-PR200	166 MHz	66 MHz	I	I	I	2.5 x	I	I
6x86MX-PR166	150 MHz	60 MHz	I	I	I	2.5 x	I	I
6x86-PR166+ 6x86L-PR166*	133 MHz	66 MHz	I	I	I	2 x	I	I
6x86-PR166+ 6x86L-PR150*	120 MHz	60 MHz	I	I	I	2 x	I	I
6x86-PR133+ 6x86L-PR133*	110 MHz	55 MHz	I	I	I	2 x	I	I
6x86-PR120+ 6x86L-PR120*	100 MHz	50 MHz	I	I	I	2 x	I	I

In Page 25, The asterisk mark "\*" with K5-PR133 has been removed. The jumper settings of K5-PR133/120/100/90/75 remain unchanged. The NOTE message now are removed.

In Page 26, the AMD-K6's voltage jumper setting is-



AMD-K6 CPU  
Top Side Marking



V (Identifier for Operation Voltage) :  
N 3.1-3.3V Core/3.135-3.6V IO  
L 2.755-3.045V Core/3.135-3.6V IO

In Page 28 and Page 30, The asterisk mark "\*" with 6x86L has been removed. The NOTE message now are removed. The new voltages for Cyrix and IBM 6x86MX-PR166/200 series CPU are 2.9V, same as above.