

To : Users of Manual VT-501

This manual have some errors need to be corrected and updated.

In **Table of Contents: Page i-** Line 3 should be "This User Manual"

Line 17 should be "Install System Memory", Line 26 should be "Install the CPU", **Page ii-** Line 20 should be "Power Block Connector"

In **Page 18:** Section "Remove SIMMs" should move to bottom of Page 17.

In **Page 47, Chipset Features Setup** Section, there is a new feature **Delayed PCI Transaction** was added.

Enable this feature to abort the current PCI master cycle and to accept the PCI master request, it reaccepts the original PCI master and returns the PCI data phase to the original PCI master.

The options are: Enabled, Disabled (Default).

CPU Jumper Settings

Intel P55C Series (page 23)

CPU Speed (MHz)	External (CPUCLK) (MHz)	CLK1	CLK2	CLK3	Internal	CPU Clock Rate	
						FREQ1	FREQ2
200	66	↑	↑	↑	3 x	↑	↑
166	66	↑	↑	↑	2.5 x	↑	↑

AMD-K5/K6 Series (page 25)

Model Name	CPU Speed	External (CPUCLK)	CLK1	CLK2	CLK3	CPU Clock Rate	
						Internal	FREQ1 FREQ2
K5-233 *	233 MHz	66 MHz	↑	↑	↑	3.5x	↑ ↑
K5-200	200 MHz	66 MHz	↑	↑	↑	3x	↑ ↑
K5-166	166 MHz	66 MHz	↑	↑	↑	2.5x	↑ ↑

* : This CPU had not yet been tested when this page was printed.

Model Name	CPU Speed	External (CPUCLK)	CLK1	CLK2	CLK3	CPU Clock Rate	
						Internal	FREQ1 FREQ2
K5-PR200	133 MHz	66 MHz	↑	↑	↑	2 x	↑ ↑
K5-PR166	118 MHz	66 MHz	↑	↑	↑	1.75 x	↑ ↑
K5-PR150	105 MHz	60 MHz	↑	↑	↑	1.75 x	↑ ↑
K5-PR133	100 MHz	66 MHz	↑	↑	↑	1.5 x	↑ ↑