

**Memory Hole At 15MB Addr.**

When enabled, every time the processor accesses the 15~16MB address, memory hole at the 15MB address will be relocated to the 15~16MB address range of the ISA cycle. When disabled, it will let the memory hole at the 15MB address decode be treated as a DRAM cycle when processor accesses the 15~16MB address.

**Cache Timing Control**

Allows you to adjust the access speed of VT82C575MV to external cache.  
The options are: Enabled, Disabled (Default).

**DRAM Timing Control**

Allows you to speed up the data access of VT82C575MV.

The options are: Normal, Fast, Turbo (Default).

**SRAM Tag/Alt Bit Config.**

Allows the alter bit to check whether or not the external cache writes back data to main memory.

The options are: 7Tags+ALT (Default), 10Tags+ALT, 8 Tags.

**Onchip IDE first channel**

When enabled, allows the IDE drive to use the PCI IDE first channel.

The options are: Enabled (Default), Disabled.

**IDE HDD Block Mode**

Allows the system to execute read/write requests to hard disk in block mode.

The options are: Enabled (Default), Disabled.

**IDE Primary Master PIO**

Allows you to select first PCI IDE channel of the primary master hard disk mode or to detect it by the BIOS. The available options are: Auto (Default), Mode 0, Mode 1, Mode 2, Mode 3, Mode 4.

**IDE Primary Slave PIO**

Allows you to select the first PCI IDE channel of the primary slave hard disk mode or to detect it by the BIOS. The available options are: Auto (Default), Mode 0, Mode 1, Mode 2, Mode 3, Mode 4.

**DRAM for BANK 0-2**

Select "EDO" when you use EDO DRAMS.

**Onboard IDE 2nd Port**

Allows you to use onboard IDE controller.

The options are: Enabled (Default), Disabled.

**Onboard FDC Control**

Allows you to enable the floppy disk controller (FDC). The options are: Enabled (Default), Disabled.

**Onboard Serial Port 1**

Enable it if serial port 1 uses the onboard I/O controller. If some I/O card to be installed, COM 3 and COM4 may be needed. Select COM 3 or COM 4 by this feature.

The options are: COM1/3F8H (Default), COM2/2F8H, COM3/3E8H, COM4/2E8H.